

HYBRID CONDUCTOR-BOARD FOR MULTI-CONDUCTOR ROUTING

FIELD OF THE INVENTION

5 The invention relates generally to multi-conductor routing schemes, and more particularly a hybrid conductor-board for straightforward and cost-effective conductor routing.

BACKGROUND OF THE INVENTION

10 Sophisticated electronic assemblies often employ dense arrays of electrical conductors to transmit signals from one area to another. Routing large groups of conductors in an efficient and organized manner often proves problematic for a variety of reasons. The overall assembly cost, formfactor (size) and complexity all must typically be taken into account to determine a suitable routing method.

15 One method of routing groups of signal conductors from one area to another involves bundling the conductors together into a harness, and selectively branching individual conductors off from the main branch as needed. This is a fairly common and well-known technique where routing space is often plentiful. Moreover, for high fidelity applications, true cables (coaxial, fiber optic, etc.) may be employed to
20 transmit high-frequency signals with the maximum possible signal quality. However, for systems and assemblies requiring high-density and compactness, harnesses are often not the best solution.

 A common method of routing large groups of signal conductors in a compact format involves the use of printed circuit boards, or PCBs. These assemblies are rigid
25 board-like structures having one or more layers. Each layer comprises a substrate formed with a pattern of conductors. Depending on the application, hundreds to thousands of conductors may be routed by a single PCB.

 For complex multi-layer boards, the conductor, or "trace" dimensions may be very small, and typically comprise a "surface-mount" structure etched or deposited
30 upon the substrate. Layer to layer connections are made through the use of vertically formed vias. High-frequency signals up to around one gigahertz may be transmitted along these traces with somewhat acceptable degradation, especially where signal and ground layers are alternated to create somewhat of a non-ideal transmission line as is well-known in the art.

Unfortunately, signals propagating above one gigahertz incur substantial losses along conventional PCB traces due to a variety of factors. In short, the physical limits of conventional PCB technology have been challenged and surpassed as signal frequencies increase for many electronic assemblies and systems. What is needed and heretofore unavailable is a hybrid harness/PCB structure that provides a straightforward, low-cost and efficient solution to the problems identified above. The hybrid conductor-board of the present invention satisfies this need.

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SUMMARY OF THE INVENTION

The hybrid conductor-board of the present invention provides a cost-effective and straightforward way to route dense arrays of high frequency conductors with minimal signal degradation.

5 To realize the foregoing advantages, the invention in one form comprises a router for funneling a plurality of conductors. The router includes a routing unit and a plurality of conductor paths. The conductor paths are directed through the routing unit and are adapted to receive the conductors. The routing unit and the plurality of conductor paths are formed by a three-dimensional fabrication process.

10 In another form, the invention comprises a hybrid conductor/board including a router having a routing unit and a plurality of conductor paths directed through the routing unit. A plurality of conductors are routed through the conductor paths.

In yet another form, the invention comprises an automatic test equipment interface for funneling signal conductors from a plurality of pin electronics boards to one or more devices-under-test. The interface includes a hybrid conductor/board comprising a signal router having a routing unit and a plurality of conductor paths directed through the routing unit. A plurality of signal conductors are routed through the conductor paths. The hybrid conductor/board has first and second planar surfaces with the first planar surface adapted for coupling to the pin electronics boards. A device interface board couples to the second planar surface and is adapted to connect to the one or more devices-under-test.

Other features and advantages of the present invention will be apparent from the following detailed description when read in conjunction with the accompanying drawings.

BRIEF DESCRIPTION OF THE DRAWINGS

The invention will be better understood by reference to the following more detailed description and accompanying drawings in which

FIG. 1 is a partial cross-sectional view of a hybrid cable-board according to
5 one form of the present invention;

FIGs. 2a - 2d are diagrams illustrating one method of fabricating the hybrid cable-board of Figure 1;

FIG. 3 is a high-level block diagram of automatic test equipment employing the hybrid cable-board of Figure 1 in an ATE interface;

10 FIG. 4 is a side view of the interface employed in the automatic test equipment interface of Figure 3; and

FIG. 5 is a partial cross-sectional view of a hybrid board according to a second embodiment of the present invention.

DETAILED DESCRIPTION OF THE INVENTION

The hybrid conductor/board of the present invention, shown in Figure 1 and generally designated 10, provides a straightforward and cost-effective way to route large quantities of signal conductors while maintaining optimal signal quality over the conductors. This is accomplished through the use of a router 20 uniquely formed to receive a plurality of conductors 24. By employing a true three-dimensional way of routing conductors on a mass scale, the complexities, costs, and performance limitations of PCB fabrication are eliminated.

Further referring to Figure 1, the signal router 20 comprises a block of rigid material formed with a plurality of open-ended hollow paths 22. The paths are constructed to complementally receive the respective signal conductors 24. Terminated at each end of the paths, the conductors are coupled to respective contact arrays 25 and 26 for interfacing with, for example, connector contacts, discrete component contacts, semiconductor package contacts, or the like. The overall shape of the router block is unimportant to the present invention.

Referring now to Figures 2A - 2D, the signal router 20 is preferably formed of a material suitable for being incrementally processed through a 3-D additive technique, such as stereolithography (SLA), selective laser scinterring (SLS), fused deposition modeling (FDM), and laminated object manufacturing (LOM), to name but a few. SLA comprises the preferred method of fabrication due to its known desirable resolution capabilities. Materials such as Vantico SL5510 or 5530 work well for the preferred embodiment.

Generally, the 3-D additive process incrementally builds-up the signal router 20 and conductor paths 22, such as that shown in Figure 2a. While only one conductor path is shown for simplicity, it should be understood that several thousand paths may be formed simultaneously through a single router. Moreover, while 3-D additive processes are preferred, 3-D subtractive processes are also envisioned within the scope of the present invention.

Once the signal router structure is complete, a slurry of fine particles may be employed, as shown in Figure 2b, to clear any debris in any of the conductor paths. Once cleared, the paths are ready for insertion of the respective signal conductors 24, shown in Figure 2c. Insertion is accomplished by using, for example, an actuated clamping collet 27, such that each conductor is gripped and pushed into the hollow path 22.

Following insertion of all of the signal conductors, the assembly may be finished depending on the application. For example, in the embodiment illustrated in Figure 1, the conductor ends are terminated, and contacts deposited on each side to form the respective contact arrays 25 and 26. Alternatively, as shown in Figure 2D,
5 pre-formed plates 28 and 29 may be attached to the top and bottom of the router, followed by milling and plating steps (not shown) to form respective interface arrays.

The hybrid conductor-board 10 (Figure 1) is beneficial for a variety of electronic and/or fiber optic applications requiring point-to-point routing and/or funneling of large numbers of conductors. Referring now to Figure 3, the invention is
10 especially useful in automatic test equipment (ATE) interface applications.

With continuing reference to Figure 3, automatic test equipment generally employs up to several thousand channels of electronic circuitry, commonly referred to as pin electronics, that interface to one or more devices-under-test (DUTs - not shown) to verify operability, etc. The equipment, generally designated 30, includes a
15 computer workstation 32 that couples to a testhead 34. The testhead encloses the channel circuitry in an effort to position the electronics as close as possible to the DUTs (which are positioned on a handler/prober 36). In order to funnel the thousand of tester signal channels from the relatively large testhead connections to the very densely packed prober or handler connections, an ATE interface 40 is interposed
20 therebetween.

Referring now to Figure 4, an ATE interface according to one form of the present invention, generally designated 40, includes a device interface board 42 comprising a stiffener 44 formed with a central opening 46 and a high-density device coupling unit 48 (such as a socket array for mounting packaged devices, or a probe
25 array for touching down on wafer contacts). A signal router 50, formed similar to the router described above, mounts to the device interface board and includes an array of coaxial cables 52 terminated along a common plane to engage corresponding contacts of the device coupling unit 48.

To provide minimal channel-to-channel delay variations, the router may be
30 formed such that each of the inserted cables are of the same length. This is straightforwardly accomplished through the use of the 3-D additive fabrication process described earlier. Moreover, the router may include an assortment of different conductors, such as twisted pair cables, coaxial cables, fiber optic cables, and the like.

Referring now to Figure 5, a further embodiment of the present invention,
35 generally designated 100, expands the concept of utilizing a 3-D fabrication process to

create conductor paths 104 in a router 102 to fluid cooling and distribution applications. Like the previously described embodiments, the router and associated paths are formed by a 3-D fabrication process. However, instead of receiving electrical conductors, the conductor paths receive fluid conductors in the form of tubes
5 106 to route fluid through the unit. The tubes provide an advantage by minimizing fluid leaks through the router.

Those skilled in the art will appreciate the many benefits and advantages afforded by the present invention. In particular, funneling large arrays of conductors from a low-density array to a high-density array may be easily accomplished by a
10 low-cost and highly manufacturable method. Additionally, by enabling the use of true transmission lines with low dielectrics as signal conductors as opposed to microstrip traces that simulate transmission lines to certain limits, signal fidelity is maximized over a wide bandwidth passing beyond several gigahertz.

While the invention has been particularly shown and described with reference
15 to the preferred embodiments thereof, it will be understood by those skilled in the art that various changes in form and detail may be made therein without departing from the spirit and scope of the invention.